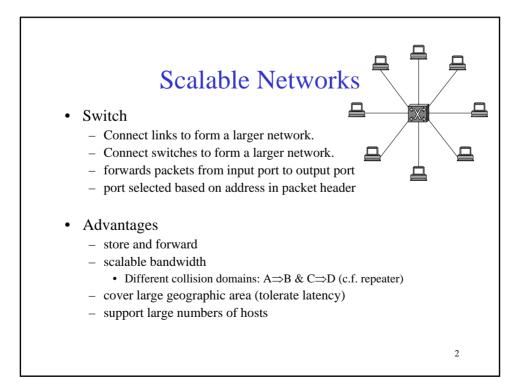
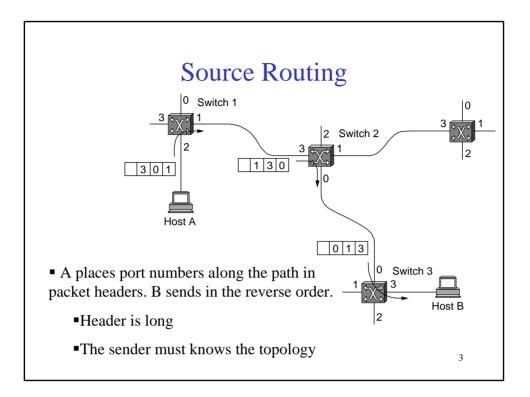
Switching and Forwarding

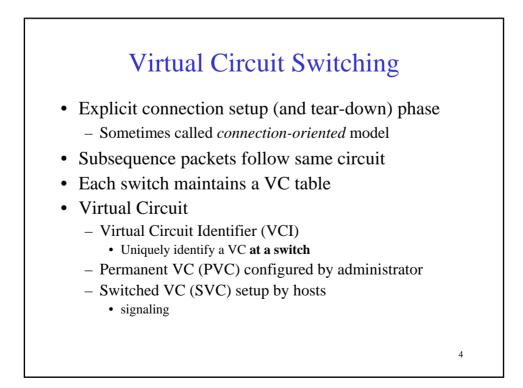
1

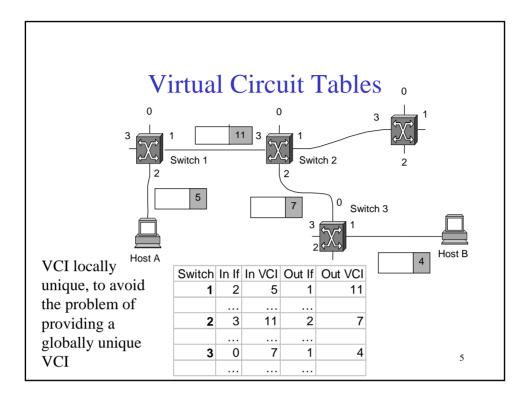
Outline

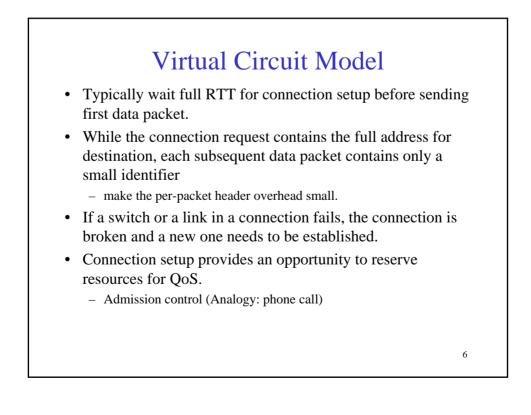
Store-and-Forward Switches Bridges and Extended LANs Cell Switching Segmentation and Reassembly

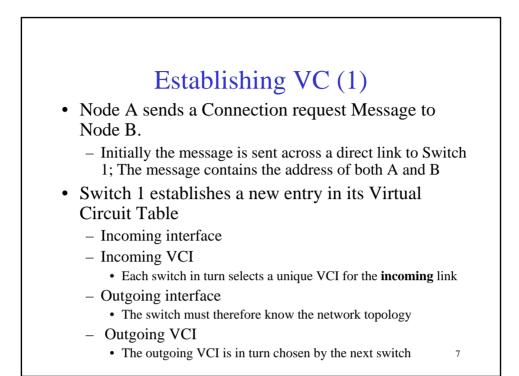


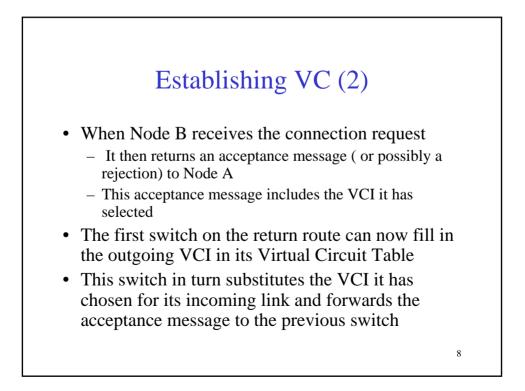


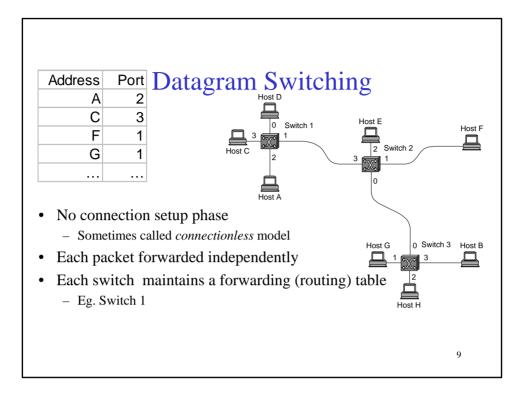


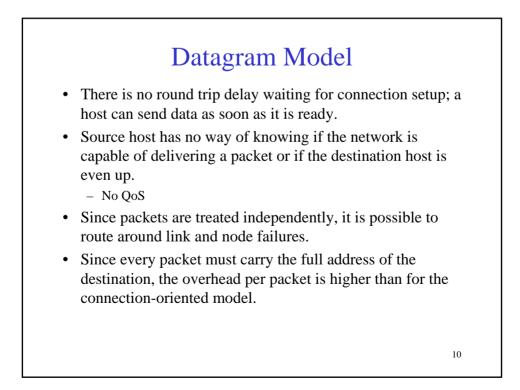


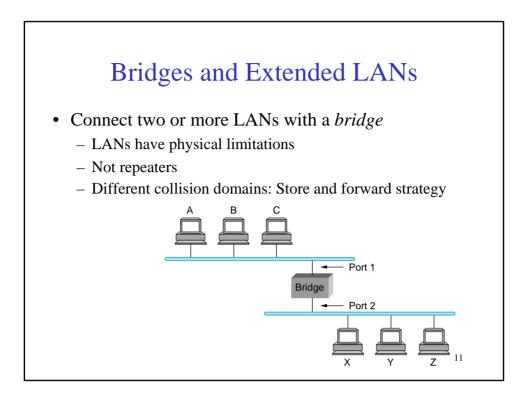


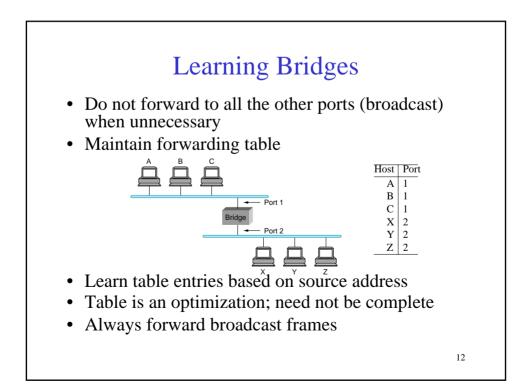


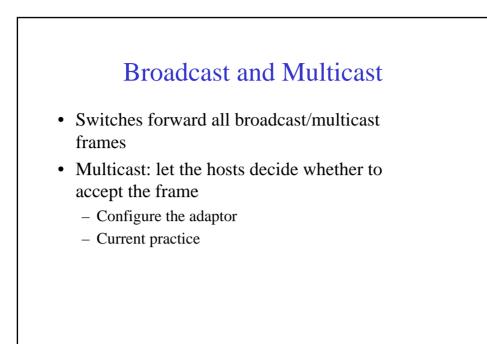


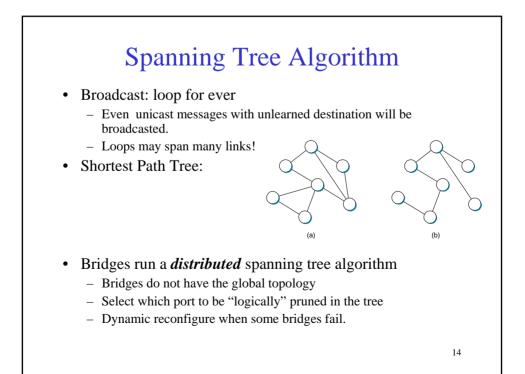


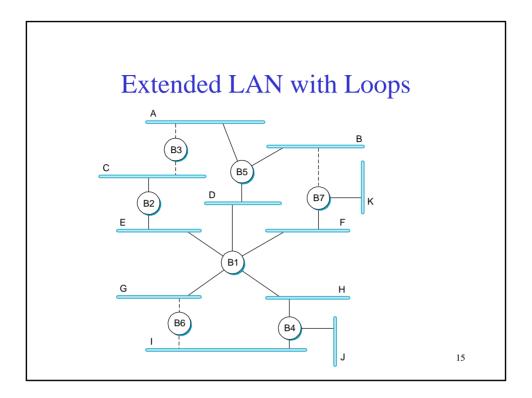


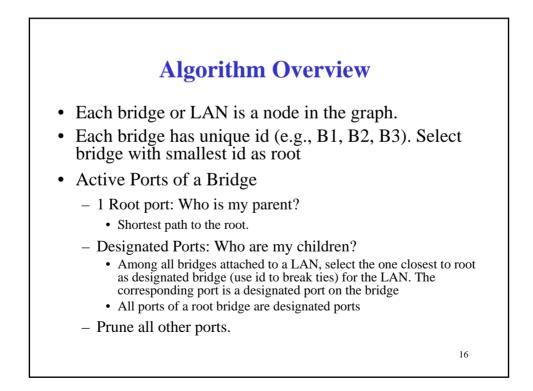


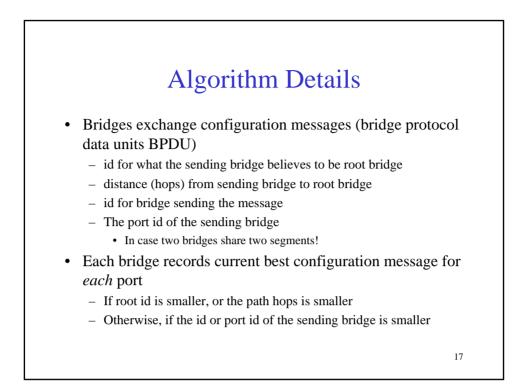


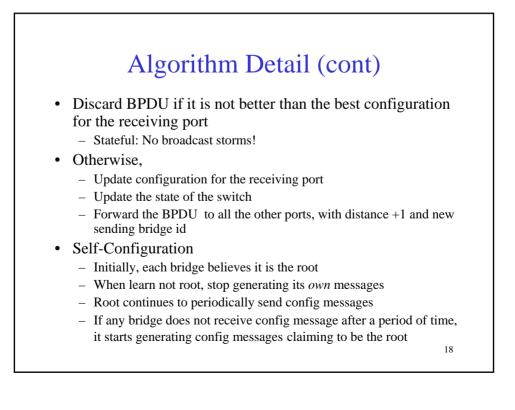


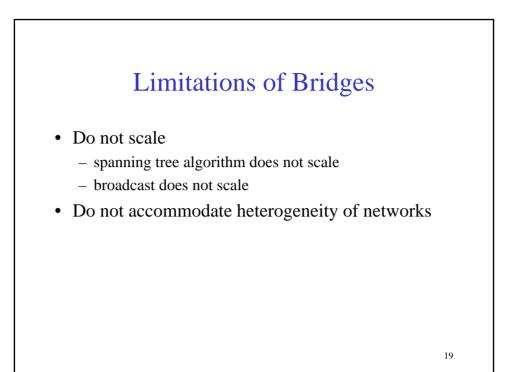


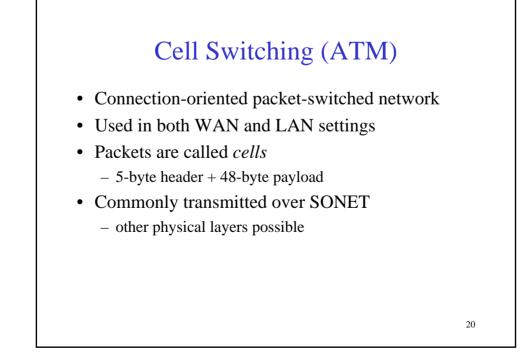








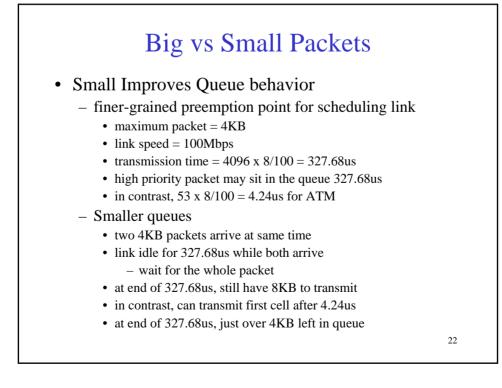




Variable vs Fixed-Length Packets

- Fixed-Length Easier to Switch in Hardware
 - Simpler with the knowledge of packet length
 - Easier to enable parallelism
- No Optimal Length for Fixed-Length Cells
 - if small: high header-to-data overhead
 - if large: low bit utilization for small messages
 - Padding: the number of valid bytes is indicated in the header

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Big vs Small (cont)

- Small Improves Latency (for voice)
 - voice digitally encoded at 64KBps (8-bit samples at 8KHz)
 - need full cell's worth of samples before sending cell
 - example: 1000-byte cells implies 125ms per cell (too long)
 - smaller latency implies no need for echo cancellers
- ATM Compromise: 48 bytes

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